



60SI-1890

Handwritten: 7/6/99, 3/26/99, RECEIVED, MAY 25 1999, GROUP 1700

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE GROUP 1700

In re Application of: Jayantha Amarasekera et al.
Serial Number: 09/000,824
Filed: 12/30/97
Examiner: C. Lu Rutt
Art Unit: 1712
Title: SILICONE COMPOSITIONS FOR HIGH VOLTAGE
INSULATOR APPLICATIONS

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Honorable Commissioner of Patents and Trademarks
Washington, DC 20231

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MAY 13 1999

Publishing Division

AMENDMENT

Dear Sir:

In response to the Office Action of February 3, 1999, please amend the above referenced application as follows:

CERTIFICATE OF MAILING (37 CFR 1.8a)

I hereby certify that this paper (along with any paper referred to as being attached or enclosed) is being deposited with the United States Postal Service on the date shown below with sufficient postage as first class mail in an envelope addressed to the: Commissioner of Patents and Trademarks, Washington, DC 20231.

Date: 5/3/99

Handwritten signature: Margaret Biernacki
Margaret Biernacki